

I2C interface communication

Liu Yik Cheng

15WAR09183

BAME 2123 Microcontroller Peripheral

Course of the Bachelor in Science (Microelectronics with Embedded Technology)

December 2015

APPENDIX

Table of content

|  |  |  |
| --- | --- | --- |
| No. | Content | Page |
| 1 | Introduction | 1-3 |
| 2. | Methodology | 4 |
|  | Interrupt Flags | 9-10 |
|  | Configuration for I2C | 11 |
|  | Configuration for GPIO | 12 |
|  | Configuration for communication using DMA (Direct Access Memory) | 13 |
| 3. | Result | 14-28 |
| 4. | Discussion | 29-30 |
| 5. | Conclusion | 31 |
| 6. | Reference | 32 |
|  | Appendix |  |